

## PowerPack\* EA-486

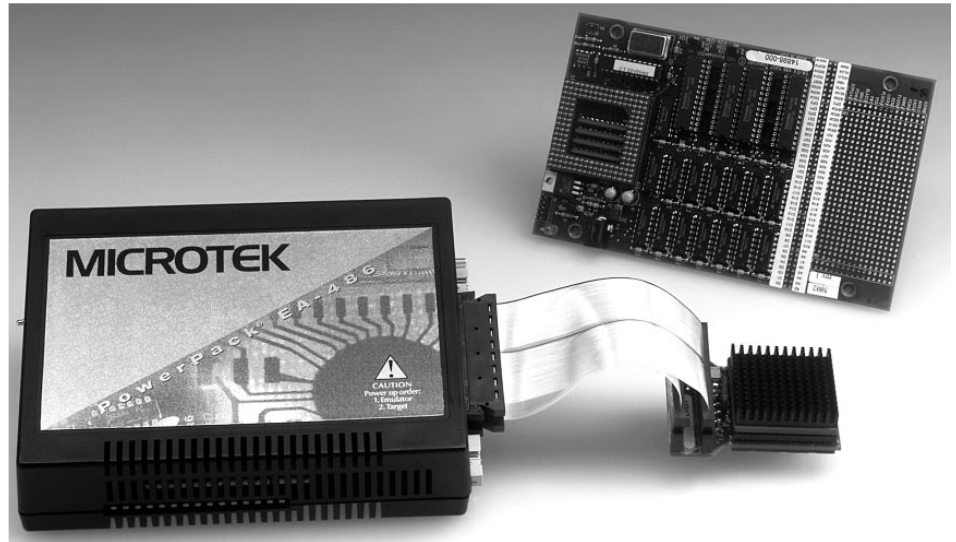
- Supports Intel486™ SX/GX, DX, SX, DX2 and SL-Enhanced Processors
- Up to 100 MHz, Zero Wait State Operation
- One and Four Megabyte Overlay Memory
- Intuitive Source-Level Debugger Under Microsoft Windows
- 256K Frame by 160 Bit-Wide Trace Buffer
- Eight Complex Events
- Four Sequential Triggers
- Exhaustive Self-Test Board Diagnostics
- Long Cable and Small Probe Head

This new high-performance emulator shows software and hardware events that are invisible with any other development tool. The combination of run control, programmable event triggers and a 160-bit wide trace buffer make it possible to discover clock-edge timing relationships of hardware signals and software symbols. Meanwhile, your interaction is transparent to the full-speed, non-stop target system.

Control execution with go, halt, goto cursor, go until/into call, return, step over, single step by line or statement. Set a breakpoint on an individual statement within a line. The variable window continuously monitors global or stack variables.

Silicon hooks in the emulator micro-processor support clock-edge event triggers linked to symbolic source-level display. Define eight events on address, data, processor status, I/O and peripheral signals. Ranges, bit masking and negation apply to both address and data. Combine these with two 16-bit or one 32-bit counter/timers to set four sequential triggers for break and trace.

In comparison, ROM monitors and software debuggers halt the processor, usurp target memory, registers and a serial port, and cannot display clock-edge hardware events. These restrictions severely limit



interactive debugging during hardware and software integration.

Full-speed trace collects data on clock cycles instead of the more conventional bus cycles to detail memory wait-states and peripheral timing. 256K trace frames which are 160 bits wide can be collected on every target cycle, as well as the more conventional bus cycle trace. Transparently supports real-mode to protected-mode transitions and accurately traces queued and cached instructions.

The entire PowerPack EA-486 emulator is smaller than most emulator pods. It measures only 7.2" long x 4.6" wide and 1.7" high. The probe head is only 3.0" long x 1.9" wide 1.1" high.

Supports Microsoft Windows\* with optional ethernet support for network environments.

PROCESSORS SUPPORTED:  
Intel486 SX/GX, DX, SX, DX2 and SL-Enhanced processors

AVAILABILITY:  
Now

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